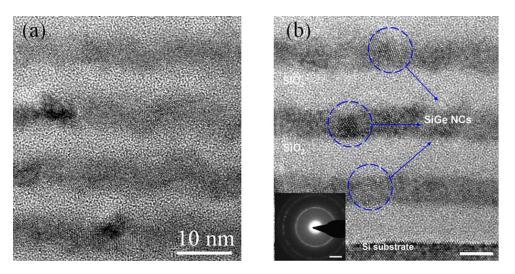
## SiGe nanocrystals embedded in SiO<sub>2</sub> for charge storage applications

EMF Vieira<sup>1,2</sup>, AG Rolo<sup>1</sup>, MJM Gomes<sup>1</sup>, and <u>A Chahboun<sup>1,3</sup></u>

- <sup>1</sup> Centre of Physics and Physics Department, University of Minho, 4710 057 Braga, Portugal
- <sup>2</sup> CMEMS-UMINHO, University of Minho, 4804 533 Guimaraes, Portugal

<sup>3</sup> FST, Univertsity Abdelmalek Essaadi, BP 416 Tangier, Morocco

Nanocrystals (NCs) based memories have emerged as a very promising alternative to the continuous floating gate. Each NC acts as a discrete charge storage node, leading to better charge retention and thus a greater reliability of the device. Tiwari and colleagues [1] proposed the first alternative memory using Si NCs as particles for charge storage. The challenge of these new technological systems is to produce regular NC's size and inter-distance.



HRTEM micrographs of SiGe NCs embedded in SiO<sub>2</sub> multilayer sample [2]

In this work,  $Si_{1-x}Ge_x NCs$  ( $0 \le x \le 1$ ) embedded in  $SiO_2$  or  $Al_2O_3$  dielectric matrices were produced by RF sputtering technique. The NCs multilayer system were characterized by XRD, Raman, and HRTEM. The retention properties were explored and discussed in relation with the growth conditions.

[1] S. Tiwari et al, Appl. Phys. Lett. 68, 1377 (1996)

[2] E. Vieira et al, J. Appl. Phys. 111, 104323 (2012)